

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

INFORMATION DISCLOSURE CITATION <i>(Use several sheets if necessary)</i>			Docket Number (Optional)		Application Number			
			13692		09/870,841			
			Applicant(s)		Richard S. NORMAN et al.			
			Filing Date		Group Art Unit		2661	
			June 1, 2001					
U.S. PATENT DOCUMENTS								
EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE	
<i>Alee</i>	1.	U.S. 5,790,539	08-04-1998	Chao et al.				
<i>Alee</i>	2.	U.S. 5,831,980	11-03-1998	Varma et al.				
<i>Alee</i>	3.	U.S. 6,069,895	05-30-2000	Ayandeh				
RECEIVED								
OCT 27 2003								
Technology Center 2600								
FOREIGN PATENT DOCUMENTS								
EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO
<i>Alee</i>	4.	EP 0 241 152 A2	10-14-1987	EUROPE				
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)								
<i>Alee</i>	5.	NOTANI H ET AL: "An 8*8 ATM switch LSI with shared multi-buffer architecture" PROCEEDINGS OF THE SYMPOSIUM ON VLSI CIRCUITS. SEATTLE, JUNE 4-6, 1992, SYMPOSIUM ON VLSI CIRCUITS, NEW YORK, IEEE, US, 4 June 1992 (1992-06-04), pages 74-75, XP010064987						
<i>Alee</i>	6.	International Search Report PCT/CA02/00810, August 1, 2003						
EXAMINER <i>Alee</i>				DATE CONSIDERED <i>08 NOV 2004</i>				

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INFORMATION DISCLOSURE CITATION
(Use several sheets if necessary)

Docket Number (Optional)

13602

Application Number

Applicant(s)

Richard S. NORMAN et al.

Filing Date

Group Art Unit

11037 U.S.P.T.O.
03/07/87 0841



U.S. PATENT DOCUMENTS

EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
Lee	1.	U.S. 5,008,878	16/04/1991	Ahmadi et al.			
Lee	2.	U.S. Re. 34,755	11/10/1994	Eng et al.			
Lee	3.	U.S. Re. 34,811	27/12/1994	Eng et al.			
Lee	4.	U.S. 5,361,257	01/11/1994	Petersen			
Lee	5.	U.S. 5,467,347	14/11/1995	Petersen			
Lee	6.	U.S. 5,999,527	07/12/1999	Petersén			
Lee	7.	U.S. 5,787,084	28/07/1998	Hoang et al.			
Lee	8.	U.S. 5,043,980	27/08/1991	Day, Jr. et al.			
Lee	9.	U.S. 6,144,662	07/11/2000	Colmant et al.			
Lee	10.	U.S. 5,938,736	17/08/1999	Muller et al.			
Lee	11.	U.S. 5,247,513	21/09/1993	Henrion et al.			

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REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						YES	NO
Lee	12. EP 0 680 173 A1	02/11/1995	EUROPE				

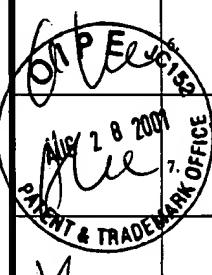
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

Lee	13.	Nick McKeown et al. "The Tiny Tera: a Packet Switch Core"; Departments of Electrical Engineering and Computer Science, Stanford University; August 1996; http://tiny-tera.stanford.edu/tiny-tera/index.html

EXAMINER	DATE CONSIDERED
	08 NOV, 2004

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INFORMATION DISCLOSURE CITATION <i>(Use several sheets if necessary)</i>		Docket Number (Optional) 13692	Application Number 09/870,841	
		Applicant(s) Richard NORMAN et al.		
		Filing Date June 1, 2001	Group Art Unit 2661 2604	
*EXAMINER INITIAL <i>Andrea</i>	OTHER DOCUMENTS <i>(Including Author, Title, Date, Pertinent Pages, Etc.)</i>			
	1.	Werner Bux et al.; Technologies and Building Blocks for Fast Packet Forwarding; IEEE Communications Magazine; January 2001; pp. 70-77.		
	2.	Minagawa, N. et al. ; Dept. of Comput. Scil, University of Electro-Commun. Tokyo, Japan; Implementation of a network switch on chips;(Abstract) Communications, vol. 13, no. 1; retrieved on March 16, 2001 from INSPEC database.		
	3.	Saturn: a terabit packet switch using dual round-robin; (abstract) Globecom'00 - IEEE, Global Telecommunications Conference; Dept. of Electr. Eng. Polytech, Univ.of Brooklyn, NY, U.S.A.; retrieved on June 4, 2001 from INSPEC database.		
	4.	Nanette J. Boden et al.; Myrinet - Gigabit-per-Second Local-Area Network [on line]; November 16, 1994 Myricom, Inc.; Internet URL http://www.myrinet.com/research/publications/Hot.ps ; retrieved on March 14, 2001;		
	5.	Vitesse Semiconductor Corporation [on line] ; Datasheet VSC880; January 5, 2001; pp. 1-20; retrieved on July 23, 2001; Internet URL www.vitesse.com/products/documents.cfm.family= document-id=180 ;		
	6.	Vitesse Semiconductor Corporation [on line] ; Datasheet VSC870; June 29, 2001; pp. 1-40; retrieved July 23, 2001; Internet URL www.vitesse.com		
	7.	A New Architecture for Switch and Router Design; PMC-Sierra Inc.; December 22, 1999; Internet URL http://www.pmc-sierra.com/pressRoom/pht/1cs_wp.pdf retrieved on July 4, 2001; pp. 1-8;		
	8.	Network Processor Designs for Next-Generation Networking Equipment [on line] ; EZ Chip Technologies; Internet URL http://www.ezchip.com/images/pdfs/etchip_white_paper.pdf ; retrieved on July 4, 2001; December 1999; pp.1-4.		
	9.	Cyrel Minkenberg et al. A combined Input and Output Queued Packet-Switched System Based on Prizma Switch-on-a-Chip Technology; Scalable High-Speed Switches/Routers with QoS Support; IBM Research, Zurich Research Laboratory; IEEE Communications Magazine; December 2000; pp 70-84;		
	10.	Werner Bux et al.; Technologies and Building Blocks for Fast Packet Forwarding; Telecommunications Networking at the Start of the 21st Century; IEEE Communications Magazine; January 2001; pp 70-77;		
	11.	Child, J.; Bus-switching chip busts bandwidth barrier [on line] ; Internet URL http://www.computer-design.com/editorial/1995/06/directions/bus.html ; retrieved on March 15, 2001.		
	12.	PSID - Based Communications Switching [on line] ; December 1997; Internet URL http://www.icube.com/commsw.pdf ; retrieved on March 15, 2001; pp 1-14		
	EXAMINER		DATE CONSIDERED <i>8 NOV 2004</i>	
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INFORMATION DISCLOSURE CITATION <i>(Use several sheets if necessary)</i>				Docket Number (Optional) 13692		Application Number 09/870,841		
				Applicant(s) Richard S. Norman et al.		Filing Date 06/01/2001		Group Art Unit Group Art Unit
U.S. PATENT DOCUMENTS								
EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE	
<i>Allen</i>	1.	U.S. 5,377,182	12/27/1994	Monacos				
<i>Allen</i>	2.	U.S. 5,278,548	01/11/1994	Haber				
<i>MAY 13 2002</i> <i>U.S. PATENT & TRADEMARK OFFICE</i> <i>RECEIVED</i> <i>MAY 14 2002</i> <i>Technology Center 2600</i>								
FOREIGN PATENT DOCUMENTS								
	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO
OTHER DOCUMENTS <i>(Including Author, Title, Date, Pertinent Pages, Etc.)</i>								
EXAMINER <i>Daniel</i>				DATE CONSIDERED <i>08 NOV, 2004</i>				

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